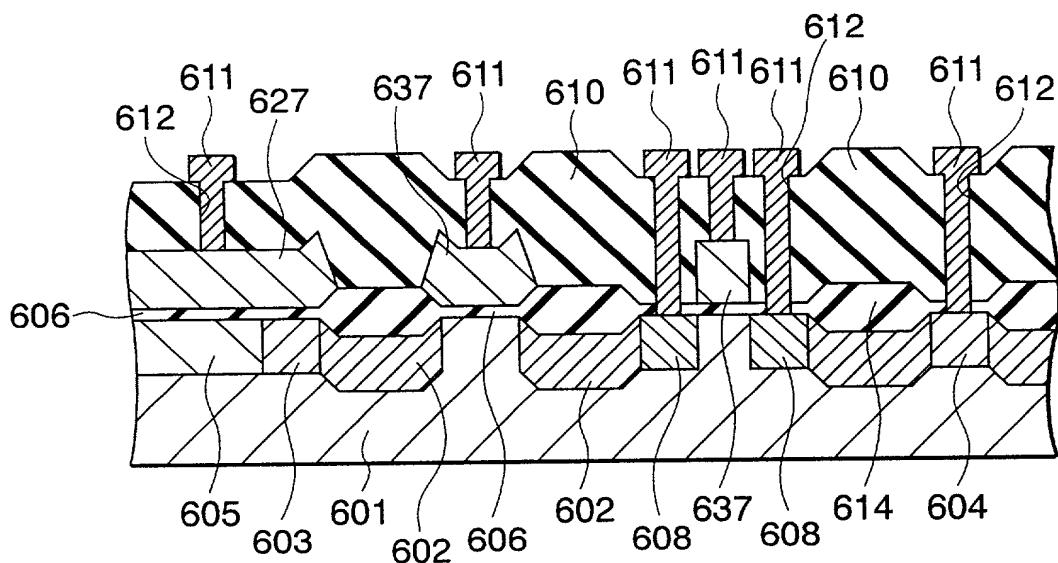
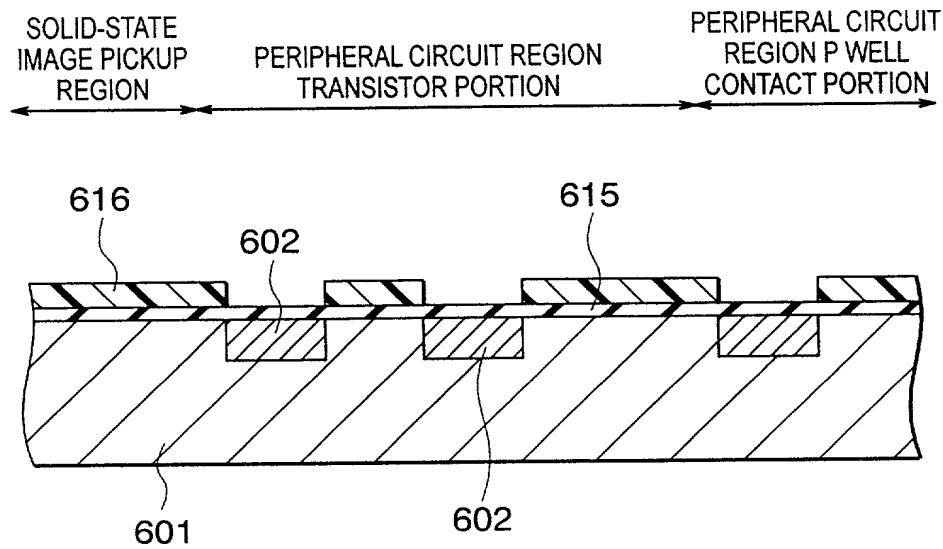


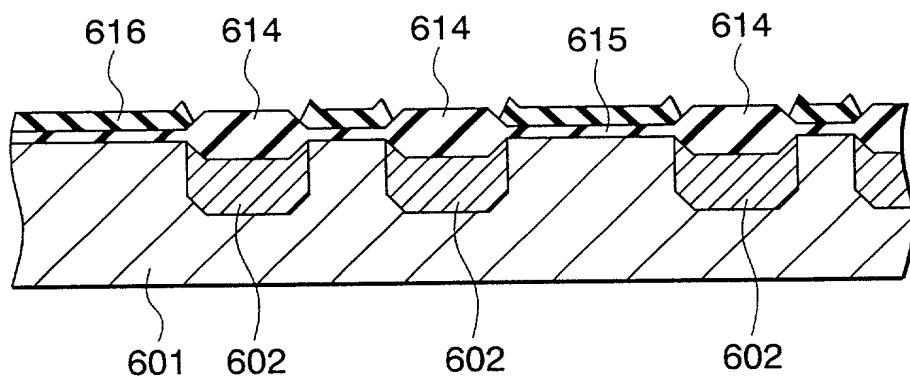
**FIG. 1**  
(PRIOR ART)



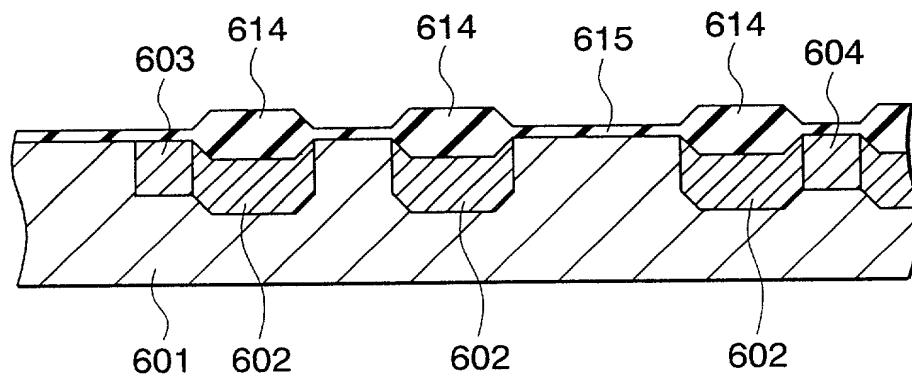
**FIG. 2A**  
(PRIOR ART)



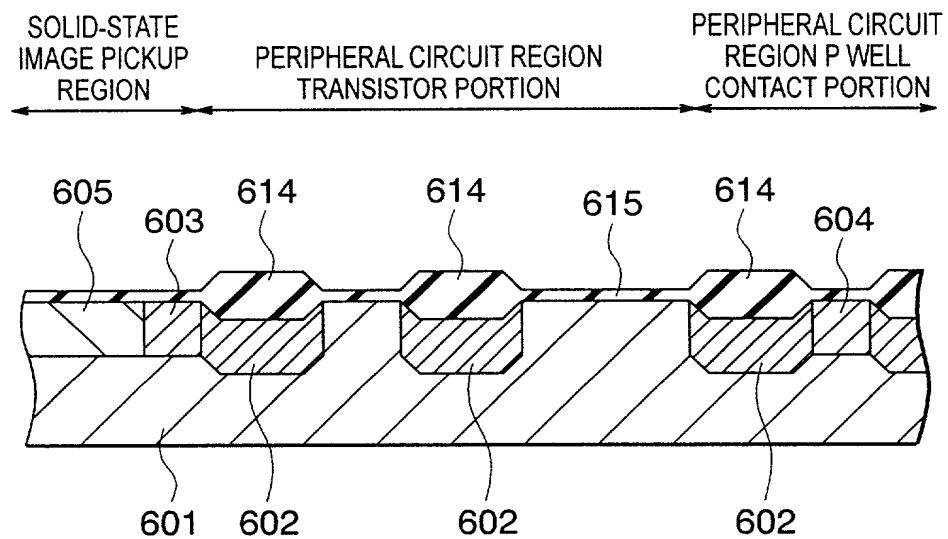
**FIG. 2B**  
(PRIOR ART)



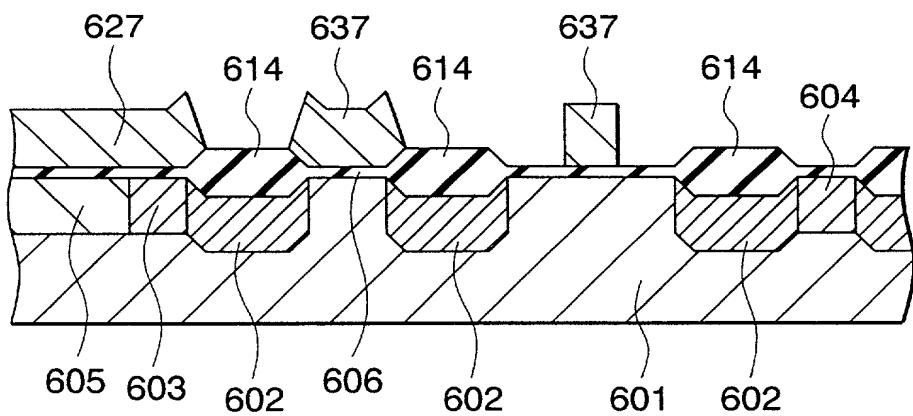
**FIG. 2C**  
(PRIOR ART)



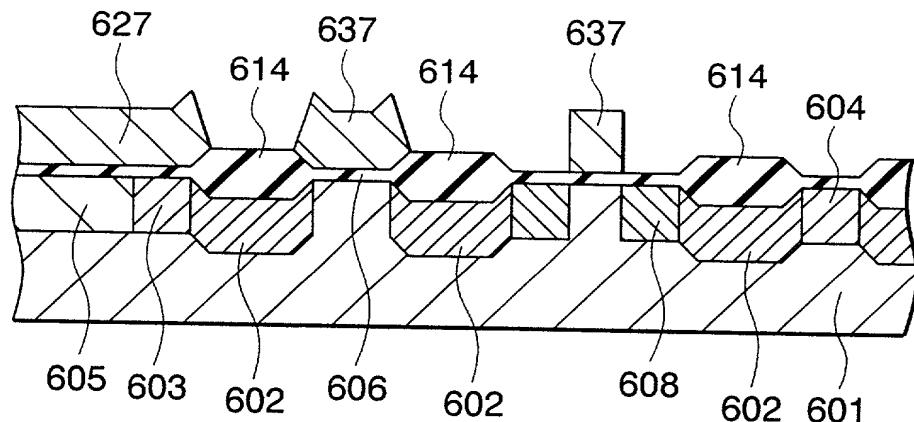
**FIG. 2D**  
(PRIOR ART)



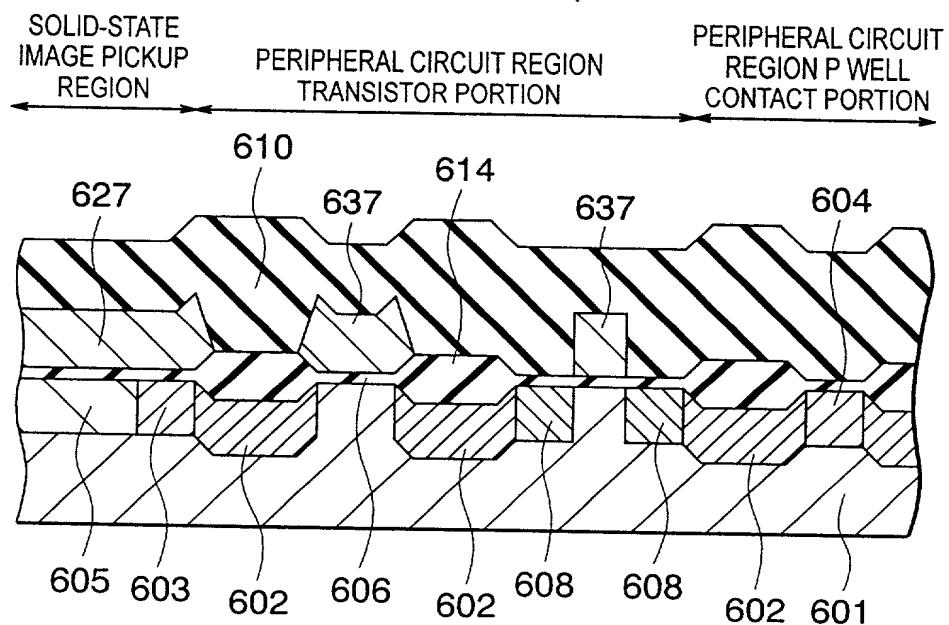
**FIG. 2E**  
(PRIOR ART)



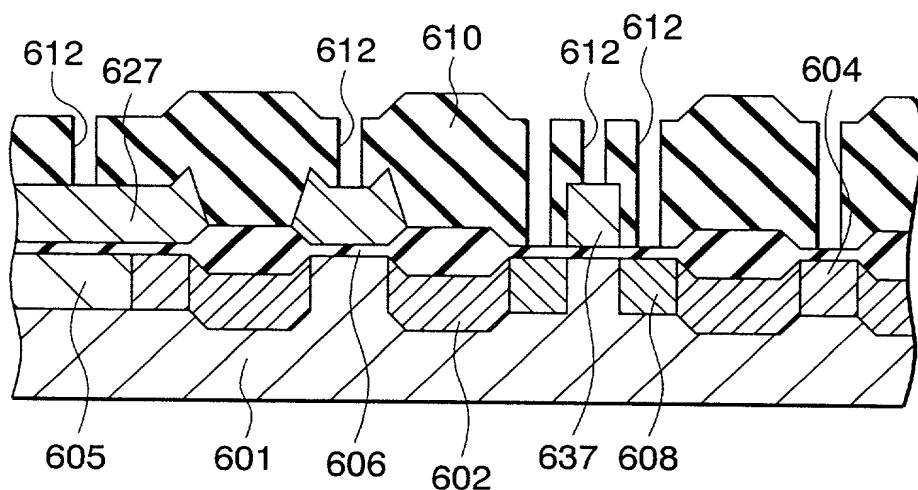
**FIG. 2F**  
(PRIOR ART)



**FIG. 2G**  
(PRIOR ART)



**FIG. 2H**  
(PRIOR ART)



**FIG. 3**

SOLID-STATE IMAGE PICKUP REGION      PERIPHERAL CIRCUIT REGION TRANSISTOR PORTION      PERIPHERAL CIRCUIT REGION P WELL CONTACT PORTION

A horizontal double-headed arrow spans across the three labeled regions: SOLID-STATE IMAGE PICKUP REGION, PERIPHERAL CIRCUIT REGION TRANSISTOR PORTION, and PERIPHERAL CIRCUIT REGION P WELL CONTACT PORTION.

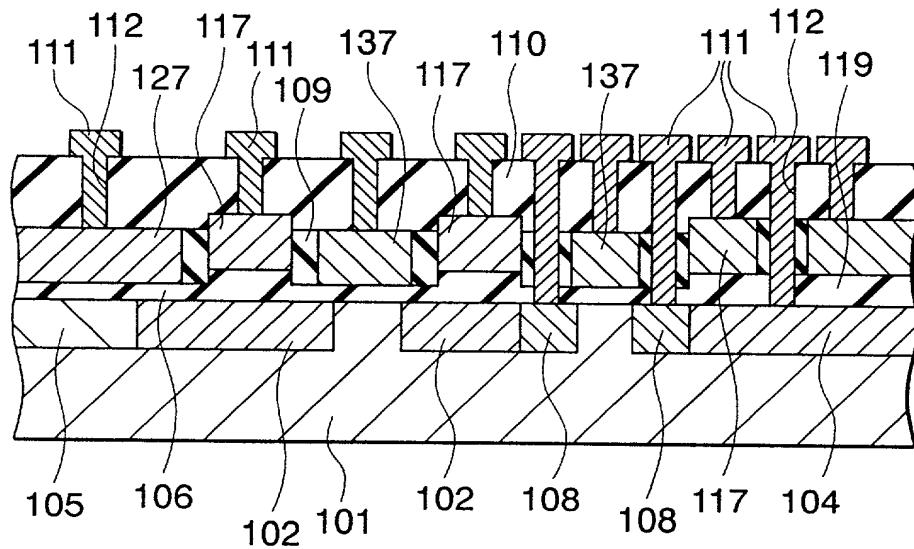


FIG. 4A

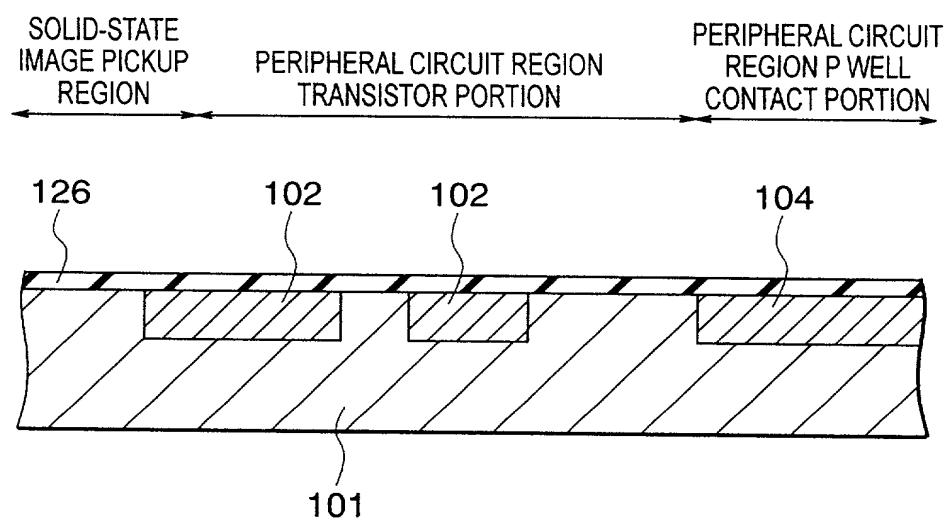


FIG. 4B

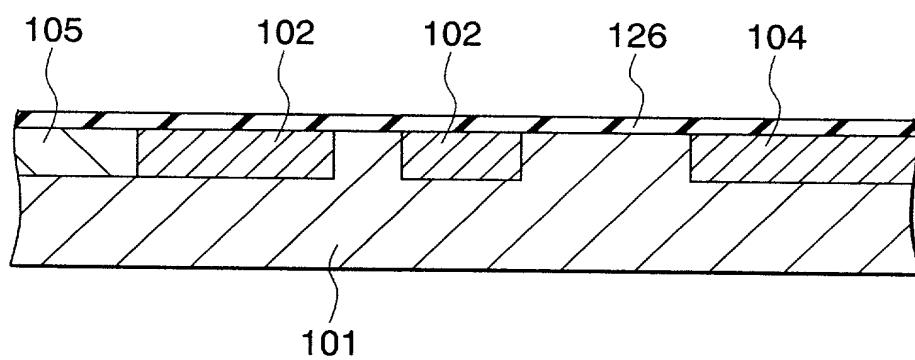
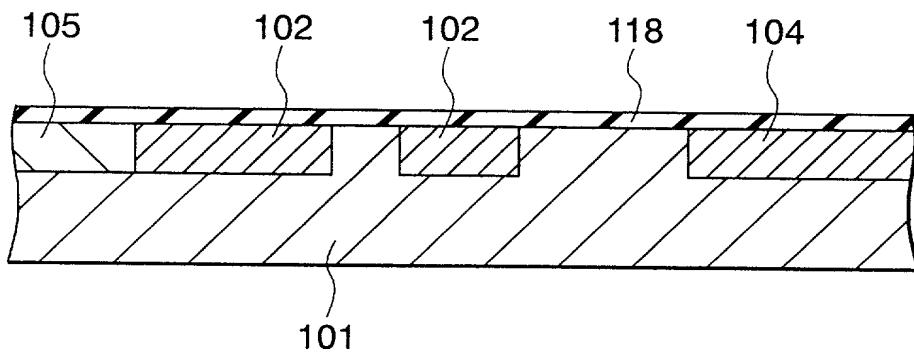
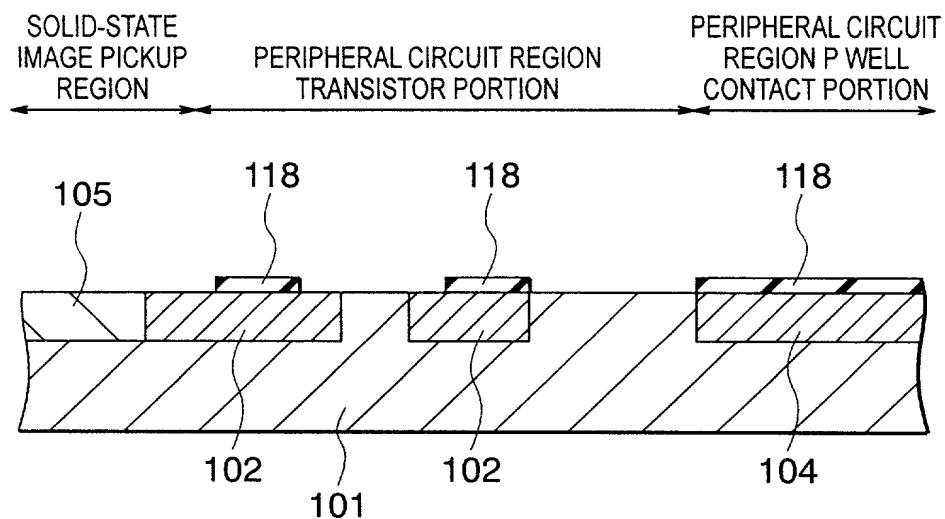


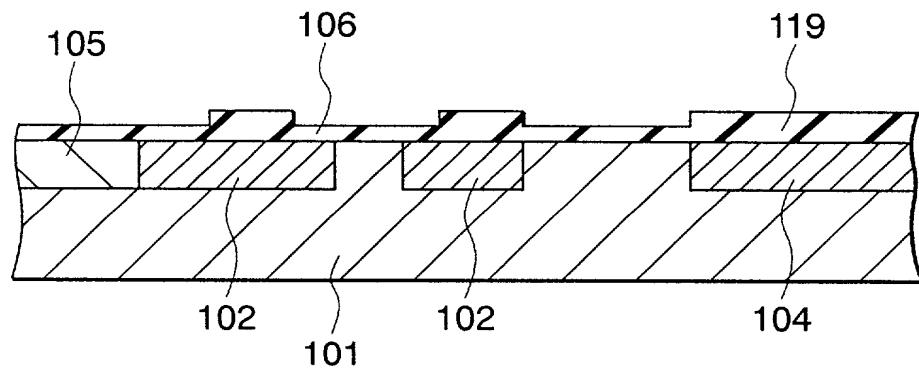
FIG. 4C



**FIG. 4D**



**FIG. 4E**



**FIG. 4F**

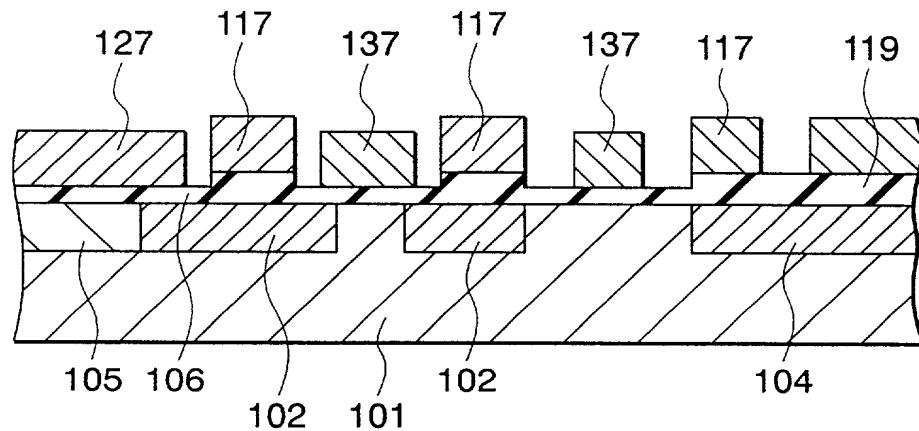


FIG. 4G

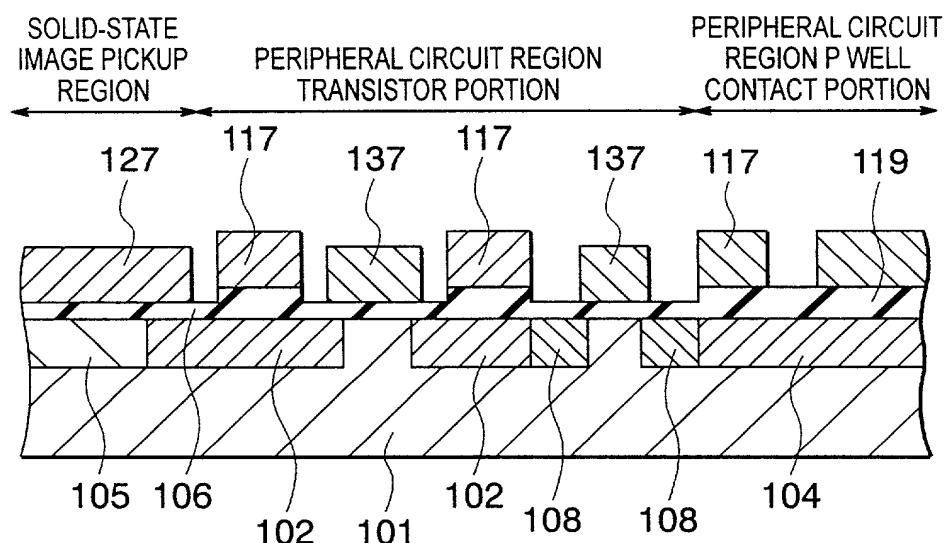


FIG. 4H

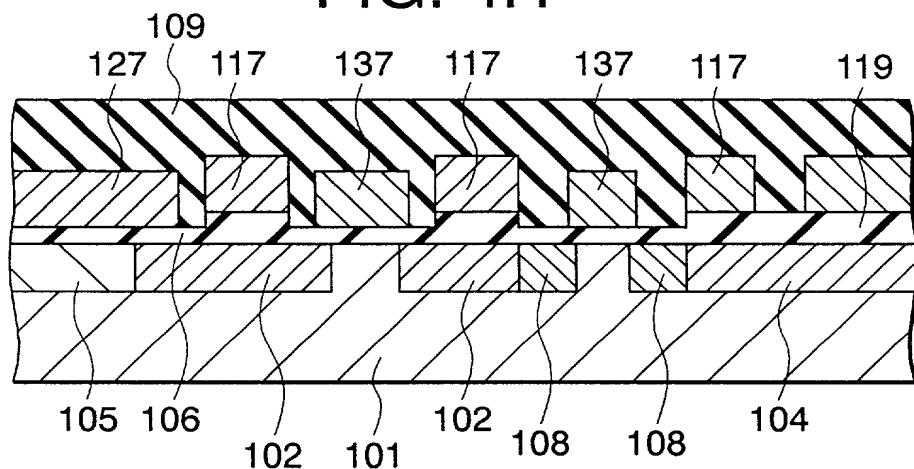


FIG. 4I

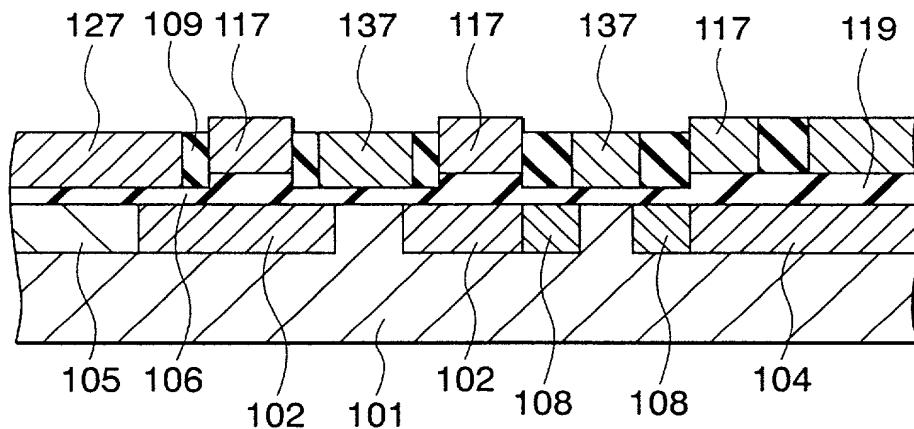


FIG. 4J

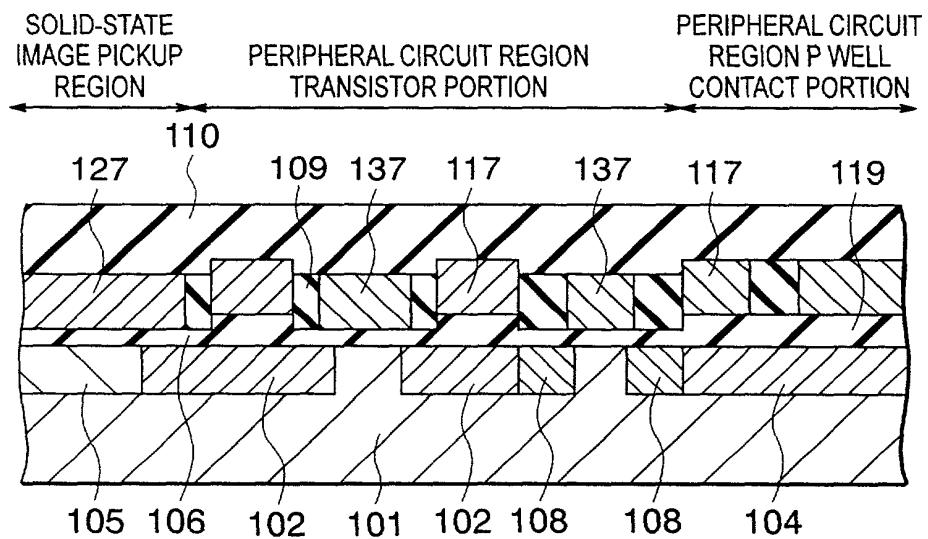


FIG. 4K

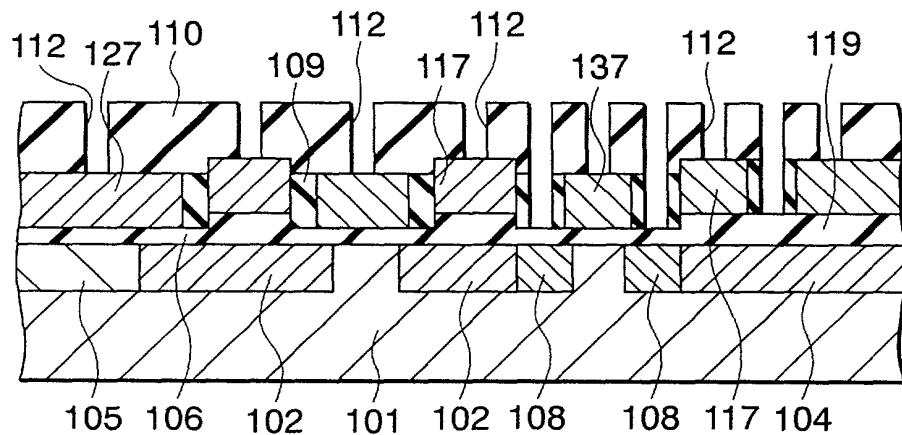


FIG. 4L

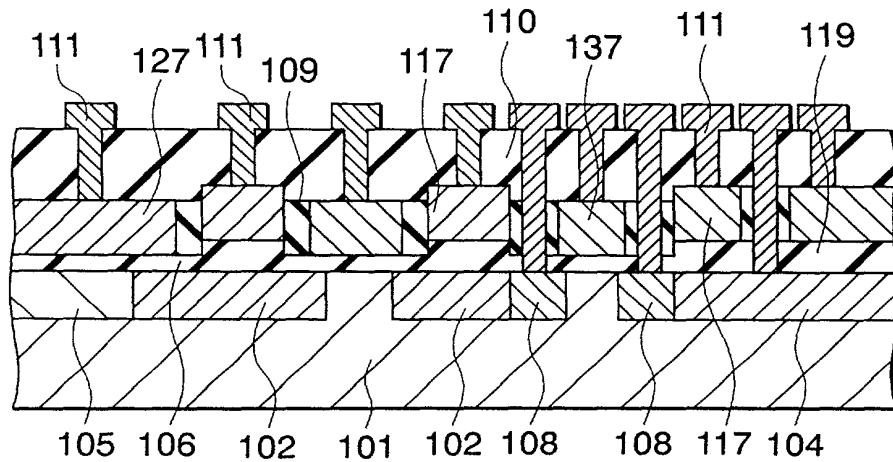


FIG. 5

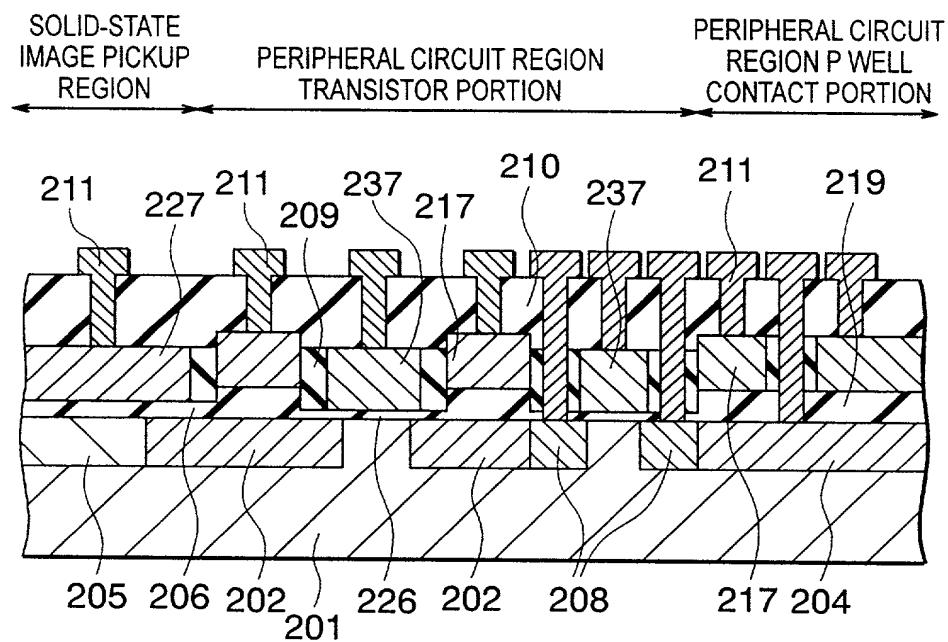


FIG. 6

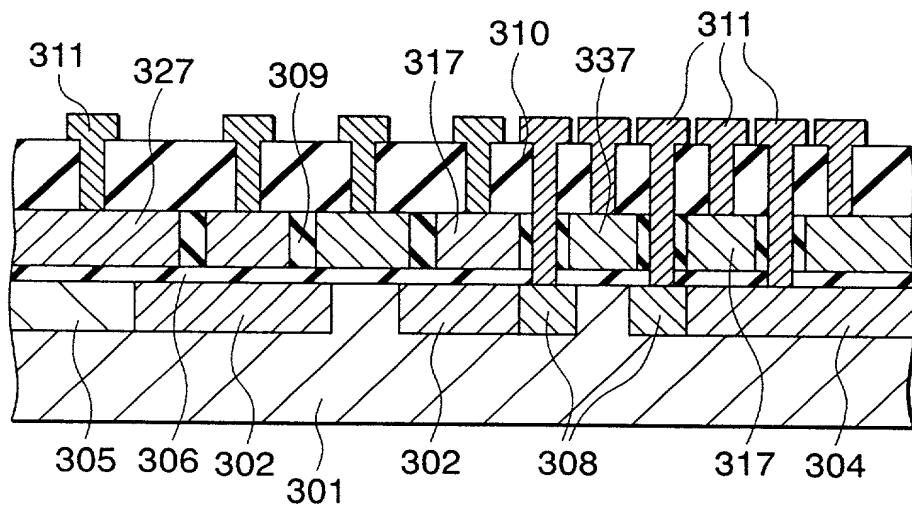


FIG. 7

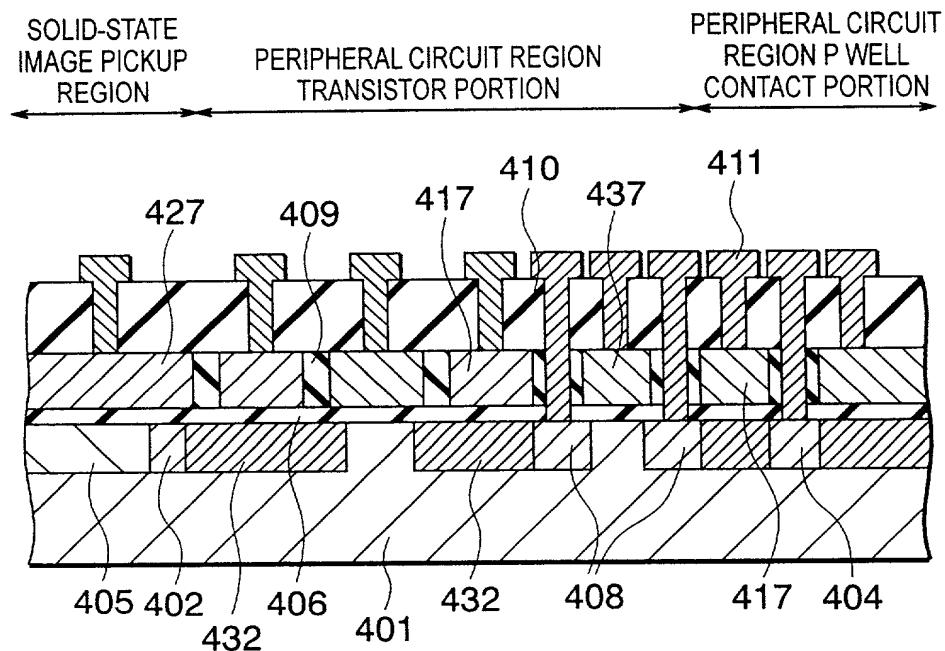


FIG. 8

